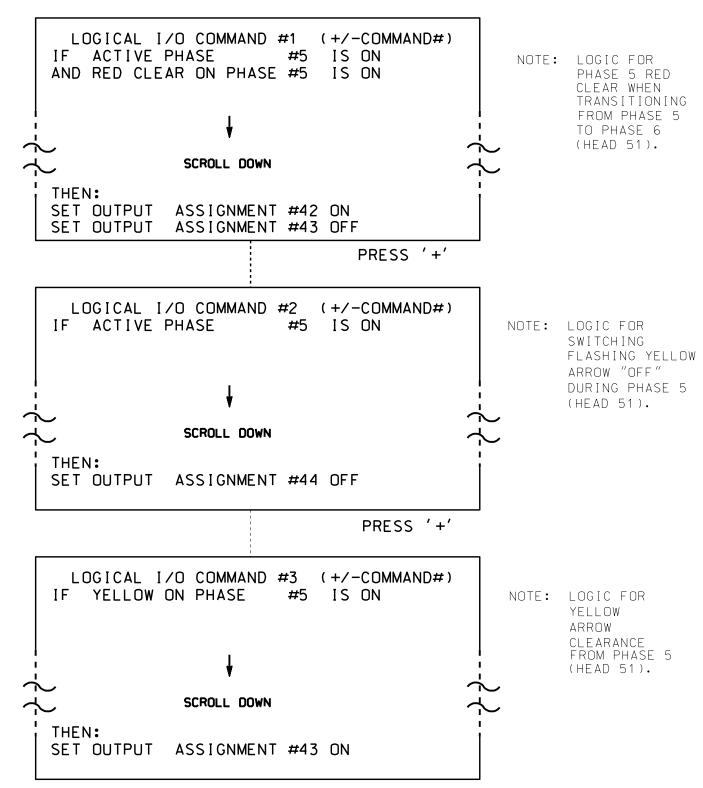
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

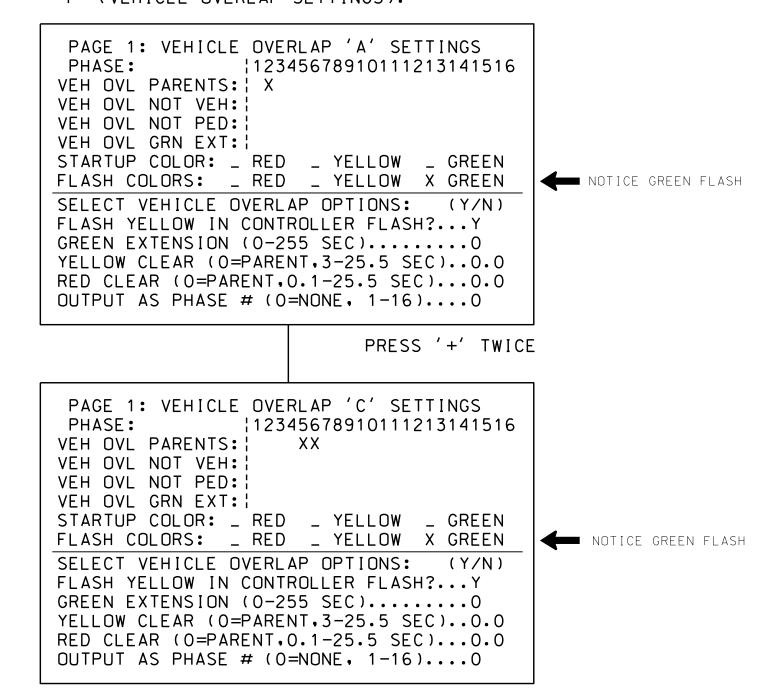
OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

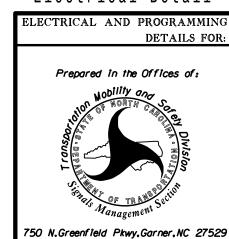
FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-0341T1 DESIGNED: September 2014 SEALED: 10/14/2014 REVISED: N/A

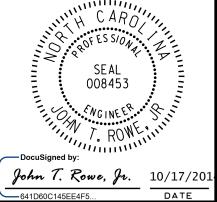
Electrical Detail - Temporary Design 1 - TCP Phase I - Sheet 2 of 2



US 221-NC 194 US 221 Business-NC 194/NC 163

ivision 11 Ashe County near_{bs}West Jefferson PLAN DATE: October 2014 REVIEWED BY: 977

prepared by: S. Armstrong | reviewed by: —— REVISIONS INIT. DATE



SIG. INVENTORY NO. 11-0341T1

SEAL