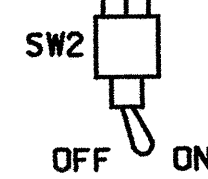


EDI MODEL 2010ECL CONFLICT MONITOR

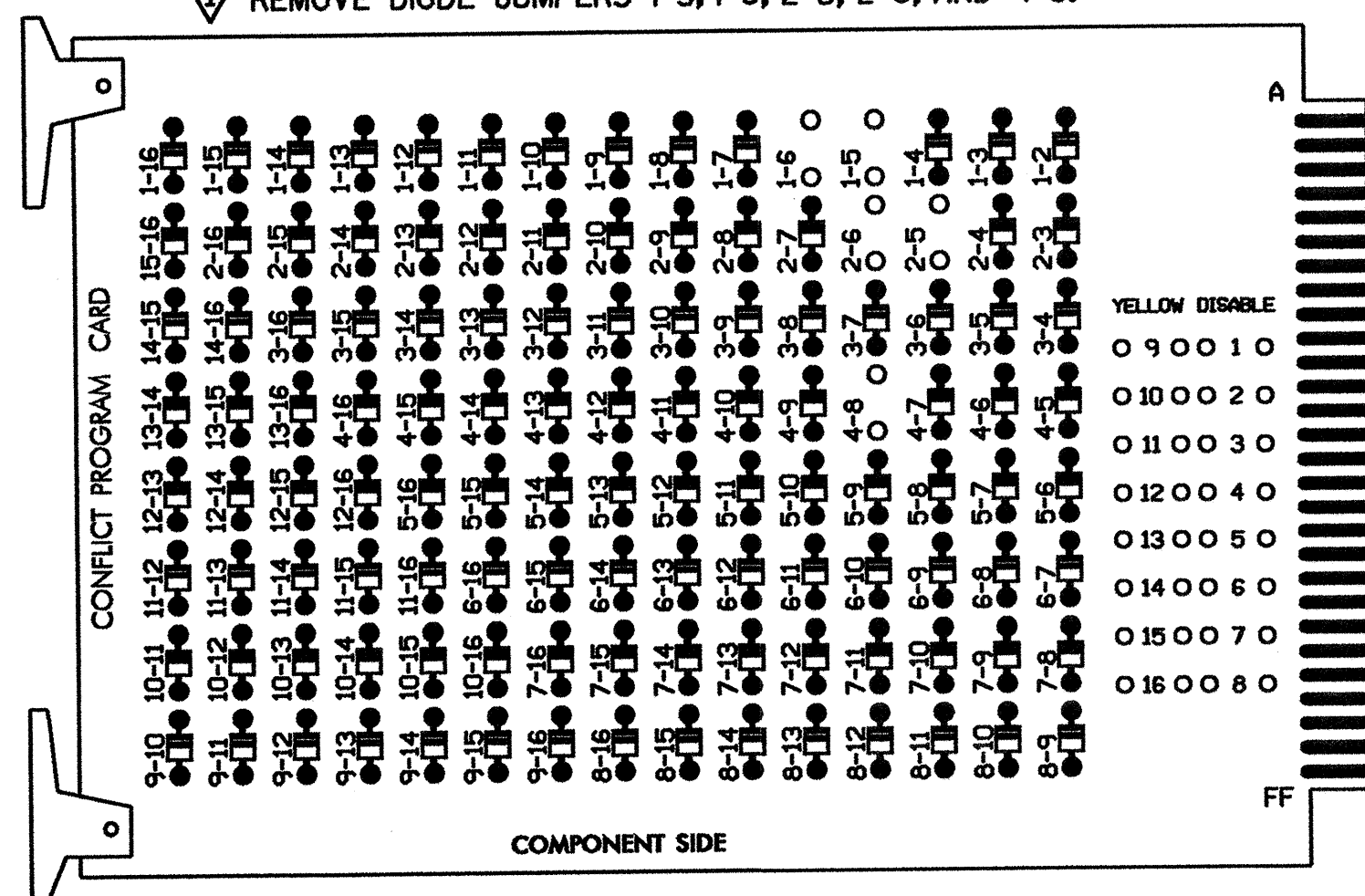
PROGRAMMING DETAIL

WD ENABLE



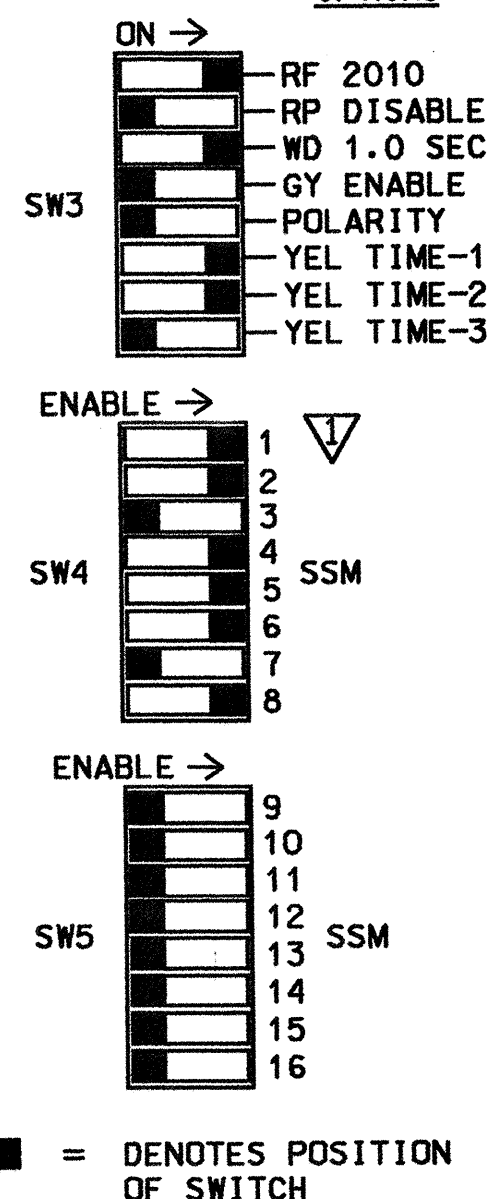
(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 2-5, 2-6, AND 4-8.



REMOVE JUMPERS AS SHOWN

OPTIONS



NOTES:

- CARD IS PROVIDED WITH ALL DIODE JUMPERS IN PLACE. REMOVAL OF ANY JUMPER ALLOWS ITS CHANNELS TO RUN CONCURRENTLY.
- MAKE SURE JUMPERS SEL1-SEL5 ARE PRESENT ON THE MONITOR BOARD.

NOTES

- TO PREVENT "FLASH-CONFLICT" PROBLEMS, INSERT RED FLASH PROGRAM BLOCKS FOR ALL UNUSED VEHICLE LOAD SWITCHES IN THE OUTPUT FILE. THE INSTALLER SHALL VERIFY THAT SIGNAL HEADS FLASH IN ACCORDANCE WITH THE SIGNAL PLANS.
- ENSURE THAT RED ENABLE IS ACTIVE AT ALL TIMES DURING NORMAL OPERATION. TO PREVENT RED FAILURES ON UNUSED MONITOR CHANNELS, TIE UNUSED RED MONITOR INPUTS 3,7,9,10,11,12,13,14,15 & 16 TO LOAD SWITCH AC+ PER CABINET MANUFACTURER'S INSTRUCTIONS.
- PROGRAM CONTROLLER TO START UP IN PHASES 2 AND 6 GREEN.
- ENABLE SIMULTANEOUS GAP-OUT FEATURE, ON CONTROLLER UNIT, FOR ALL PHASES.
- PROGRAM PHASES 4 AND 8, ON CONTROLLER UNIT, FOR DUAL ENTRY.
- PROGRAM PHASES 2 AND 6, ON CONTROLLER UNIT, FOR VARIABLE INITIAL AND GAP REDUCTION.

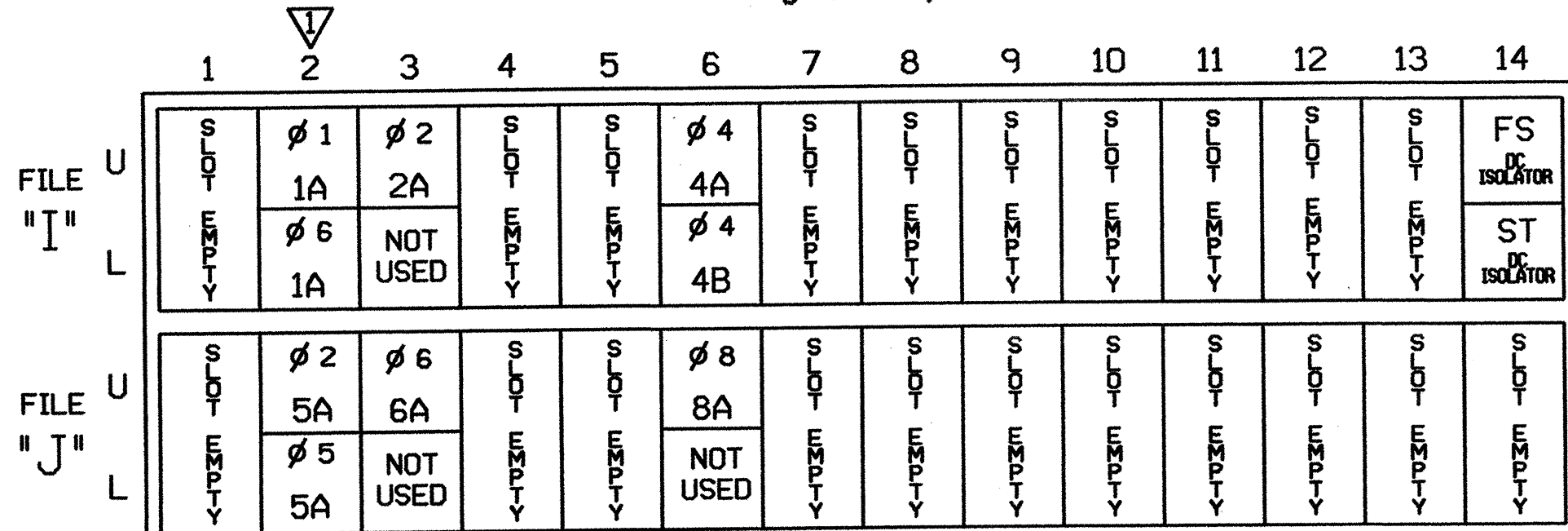
FIELD CONNECTION HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	61	21,22	NU	NU	41,42	NU	21	61,62	NU	NU	81,82	NU
GREEN		130			103			136			109	
YELLOW		129			102			135			108	
RED	*	128			101		*	134			107	
RED ARROW												
YELLOW ARROW	126							132				
GREEN ARROW	127							133				

NU = NOT USED
* DENOTES INSTALL LOAD RESISTOR. SEE LOAD RESISTOR INSTALLATION DETAIL THIS PAGE.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

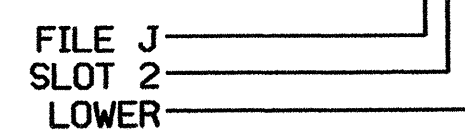
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ²	TB2-5,6	I2U	39	1	2	1	Y	Y			15
	TB2-7,8	I2L	43	5	12	6	Y	Y	Y		3
2A	TB2-9,10	I3U	63	25	32	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			10
5A ¹	TB3-5,6	J2U	40	2	6	2	Y	Y	Y		3
	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3

¹ ADD JUMPERS FROM TB3-5 TO TB3-7, AND FROM TB3-6 TO TB3-8.

² ADD JUMPERS FROM TB2-5 TO TB2-7, AND FROM TB2-6 TO TB2-8.

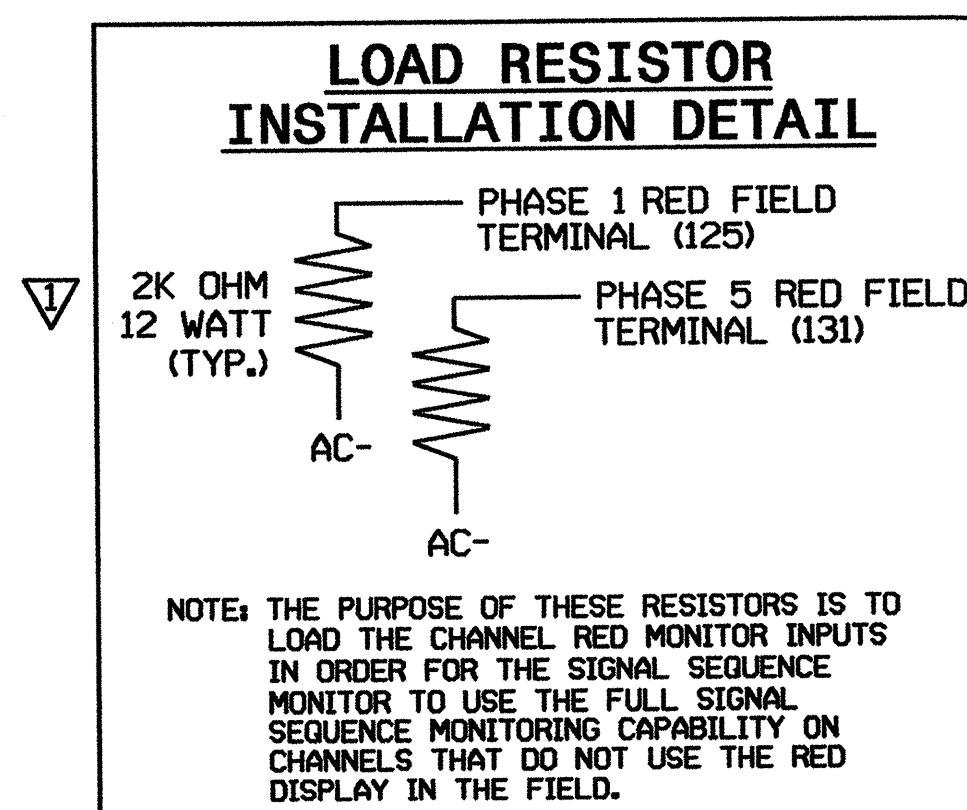
INPUT FILE POSITION LEGEND: J2L



EQUIPMENT INFORMATION

CONTROLLER.....CONTRACTOR SUPPLIED 2070L
CABINETCONTRACTOR SUPPLIED 332
SOFTWAREECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...12
LOAD SWITCHES USED.....S1,S2,S4,S5,S6,S8
PHASES USED.....1,2,4,5,6,8
OVERLAPS.....NONE

LOAD RESISTOR INSTALLATION DETAIL



NOTE: THE PURPOSE OF THESE RESISTORS IS TO LOAD THE CHANNEL RED MONITOR INPUTS IN ORDER FOR THE SIGNAL SEQUENCE MONITOR TO USE THE FULL SIGNAL SEQUENCE MONITORING CAPABILITY ON CHANNELS THAT DO NOT USE THE RED DISPLAY IN THE FIELD.

BACK-UP PROTECTION PROGRAMMING DETAIL

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE DYNAMIC/BACKUP CONTROL FUNCTIONS 1 AND 2.
- FROM PHASE CONTROL FUNCTIONS MENU PRESS '2' (DYNAMIC/BACKUP CONTROL FUNCTIONS).

DYNAMIC/BACKUP CONTROL FUNCTION #01
OVERLAPS: ABCDEFGHIJKLMNP
IF OVERLAPS ARE ACTIVE :
OR PHASES: 12345678910111213141516
IF PHASES ARE ON: X
OMIT PHASES : X
CALL PHASES : X

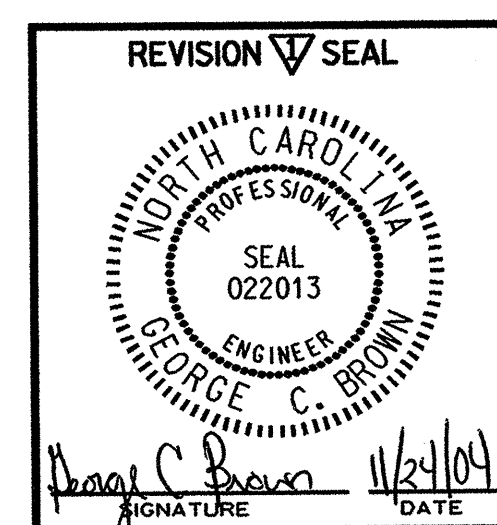
PRESS 'NEXT'

DYNAMIC/BACKUP CONTROL FUNCTION #02
OVERLAPS: ABCDEFGHIJKLMNP
IF OVERLAPS ARE ACTIVE :
OR PHASES: 12345678910111213141516
IF PHASES ARE ON: X
OMIT PHASES : X
CALL PHASES : X

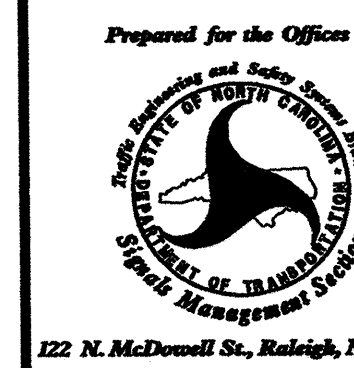
BACKUP PROTECTION PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0954T1
DESIGNED: 03/25/2003
SEALED: 2/26/2004
REVISED: 11/17/04

TEMPORARY SIGNAL 1 - PHASE I - TCP 6



PLANS PREPARED BY:
RUMMEL KLEPPER & KAHL, LLP
consulting engineers
5900 FARRINGTON PLACE SUITE 105
RALEIGH, NORTH CAROLINA 27609-3980
FOR
DIVISION OF HIGHWAYS



NC 146 (LONG SHOALS ROAD)
AT
CP&L DRIVE / ENTRANCE TO
"THE FOREST" APARTMENT COMPLEX

PLANNING	DESIGN	CONSTRUCTION
DATE: JUNE 2003	DATE: JUNE 2003	DATE: JUNE 2003
BY: MW YALCH	BY: JO DEATON	BY: JO DEATON

CHANGED FROM A 3 PHASE TO A FIVE PHASE SIGNAL (LIP)
DATE: 11/21/04

SEAL
Not a certified document as to the original document but only as to the revisions - This document originally issued and sealed by James G. Deaton, #07438, on 02/26/04. This document is only certified as to the revisions.
SIGNATURE: _____ DATE: _____
SIG. INVENTORY NO. 13-0954T1