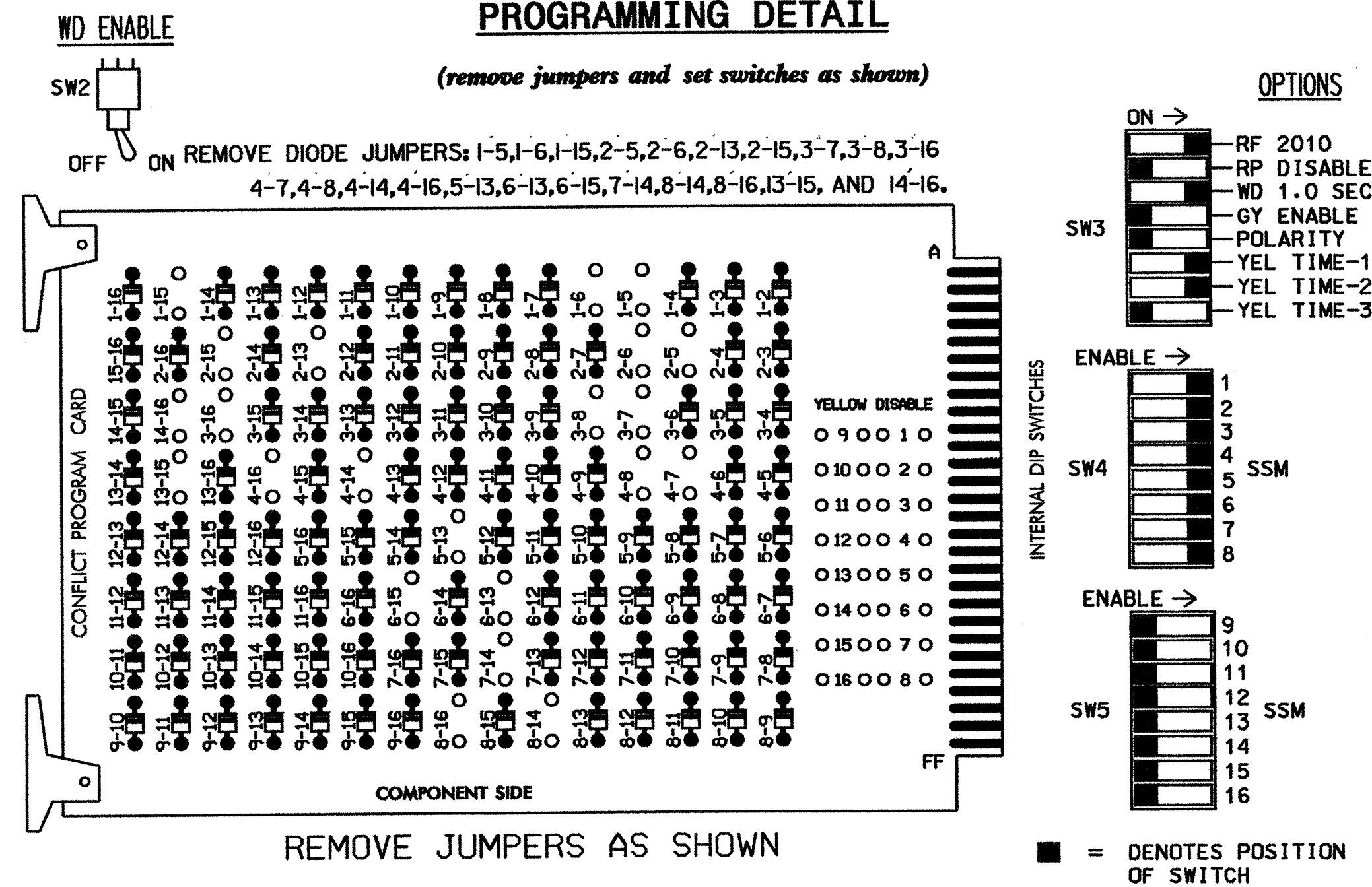


EDI MODEL 2010ECL CONFLICT MONITOR

PROGRAMMING DETAIL



NOTES:

- CARD IS PROVIDED WITH ALL DIODE JUMPERS IN PLACE. REMOVAL OF ANY JUMPER ALLOWS ITS CHANNELS TO RUN CONCURRENTLY.
- MAKE SURE JUMPERS SEL1-SEL5 ARE PRESENT ON THE MONITOR BOARD.

NOTES

- TO PREVENT "FLASH-CONFLICT" PROBLEMS, INSERT RED FLASH PROGRAM BLOCKS FOR ALL UNUSED VEHICLE LOAD SWITCHES IN THE OUTPUT FILE. THE INSTALLER SHALL VERIFY THAT SIGNAL HEADS FLASH IN ACCORDANCE WITH THE SIGNAL PLANS.
- ENSURE THAT RED ENABLE IS ACTIVE AT ALL TIMES DURING NORMAL OPERATION. TO PREVENT RED FAILURES ON UNUSED MONITOR CHANNELS, TIE UNUSED RED MONITOR INPUTS 9,10, 11,12,13,14,15 & 16 TO LOAD SWITCH AC+ PER THE CABINET MANUFACTURER'S INSTRUCTIONS.
- PROGRAM CONTROLLER TO START UP IN PHASES 2 AND 6 GREEN.
- ENABLE SIMULTANEOUS GAP-OUT FEATURE, ON CONTROLLER UNIT, FOR ALL PHASES.
- PROGRAM PHASES 4 AND 8, ON CONTROLLER UNIT, FOR DUAL ENTRY.
- PROGRAM PHASES 2 AND 6, ON CONTROLLER UNIT, FOR VARIABLE INITIAL AND GAP REDUCTION.
- PROGRAM PHASES 2, 4, 6 AND 8 FOR 'STARTUP PED CALL'.
- THE CABINET AND CONTROLLER ARE PART OF THE HOPE MILLS CLOSED LOOP SYSTEM.

EQUIPMENT INFORMATION

CONTROLLER.....CONTRACTOR SUPPLIED 2070L
 CABINETCONTRACTOR SUPPLIED 332
 SOFTWAREECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...12
 LOAD SWITCHES USED.....S1,S2,S2P,S3,S4,S4P,S5,S6,S6P,S7,S8,S8P
 PHASES USED.....1,2,2PED,3,4,4PED,
 5,6,6PED,7,8,8PED
 OVERLAPS.....NONE

FIELD CONNECTION HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	
SIGNAL HEAD NO.	61,83	21,22	P21, P22	81	41,42, 43,44	P41, P42	43	51,52	61, 62,63	P61, P62	41,63	81,82, 83,84	P81, P82
GREEN		130			103				136			109	
YELLOW		129			102				135			108	
RED	*	128		*	101				134	*		107	
RED ARROW									131				
YELLOW ARROW	126			117			132	132			123		
GREEN ARROW	127			118			133	133			124		
			115			106				121		112	
			113			104				119		110	

NU = NOT USED

* DENOTES INSTALL LOAD RESISTOR. SEE LOAD RESISTOR INSTALLATION DETAIL THIS PAGE.

BACK-UP PROTECTION PROGRAMMING DETAIL

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE DYNAMIC/BACKUP CONTROL FUNCTIONS 1, 2 AND 3.
- FROM PHASE CONTROL FUNCTIONS MENU PRESS '2' (DYNAMIC/BACKUP CONTROL FUNCTIONS).

INPUT FILE POSITION LAYOUT

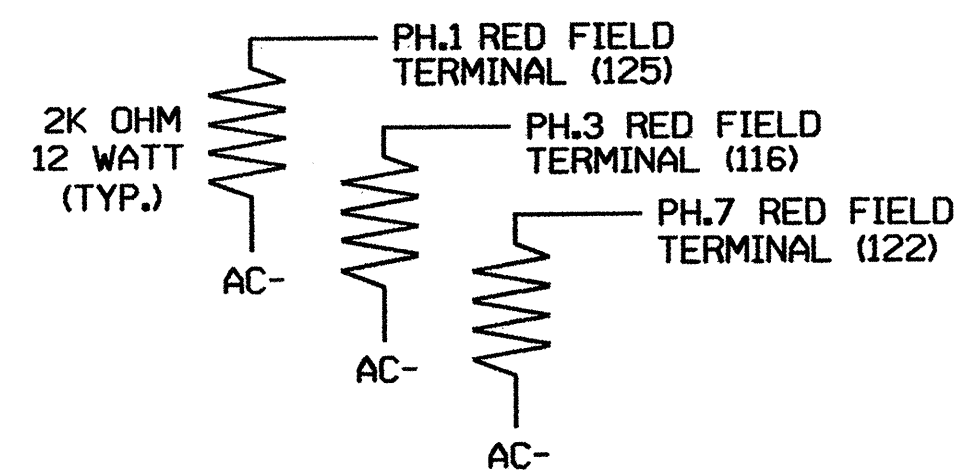
(front view)

FILE U	1	2	3	4	5	6	7	8	9	10	11	12	13	14
1B	φ1	φ6	φ2/SYS	φ8	φ4	φ3	φ4					φ2 PED	φ6 PED	FS
1A	φ1	φ2/SYS	φ3	φ4								φ4 PED	φ8 PED	ST
2A/S03														
2B/S04														
5C	φ5	φ5	φ6/SYS	φ4	φ8	φ7	φ8							
5A	φ5	φ6/SYS	φ7	φ8										
5B	φ5	φ6/SYS	φ7	φ8										

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

LOAD RESISTOR INSTALLATION DETAIL



NOTE: THE PURPOSE OF THESE RESISTORS IS TO LOAD THE CHANNEL RED MONITOR INPUTS IN ORDER FOR THE SIGNAL SEQUENCE MONITOR TO USE THE FULL SIGNAL SEQUENCE MONITORING CAPABILITY ON CHANNELS THAT DO NOT USE THE RED DISPLAY IN THE FIELD.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0455
 DESIGNED: MAY 2003
 SEALED: 10-22-03
 REVISED:

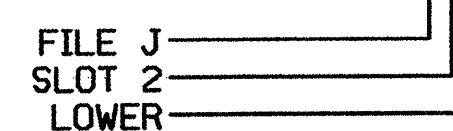
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1B	TB2-1,2	I1U	56	18	1	1	Y	Y			15
1A ¹	TB2-5,6	I2U	39	1	2	6	Y	Y	Y		3
	TB2-7,8	I2L	43	5	12	1	Y	Y			15
2A/S03	TB2-9,10	I3U	63	25	32	2/SYS	Y	Y			
2B/S04	TB2-11,12	I3L	76	38	42	2/SYS	Y	Y			
3A ²	TB4-9,10	I6U	41	3	4	8	Y	Y			
	TB4-11,12	I6L	45	7	14	3	Y	Y			15
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			
5C	TB3-1,2	J1U	55	17	5	5	Y	Y			15
5A	TB3-5,6	J2U	40	2	6	5	Y	Y			
5B	TB3-7,8	J2L	44	6	16	5	Y	Y			
6A/S01	TB3-9,10	J3U	64	26	36	6/SYS	Y	Y			
6B/S02	TB3-11,12	J3L	77	39	46	6/SYS	Y	Y			
7A ³	TB5-9,10	J6U	42	4	8	4	Y	Y			
	TB5-11,12	J6L	46	8	18	7	Y	Y			15
8A	TB7-1,2	J7U	66	28	38	8	Y	Y			
8B	TB7-3,4	J7L	79	41	48	8	Y	Y			
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- ADD JUMPERS FROM TB2-5 TO TB2-7, AND FROM TB2-6 TO TB2-8.
- ADD JUMPERS FROM TB4-9 TO TB4-11, AND FROM TB4-10 TO TB4-12.
- ADD JUMPERS FROM TB5-9 TO TB5-11, AND FROM TB5-10 TO TB5-12.

INPUT FILE POSITION LEGEND: J2L



SIGNAL UPGRADE: FINAL DESIGN

Electrical and Programming Details For:

Prepared in the Office of:

HOPE MILLS BYPASS/ SR 1363 (ELK ROAD) AT SR 1132 (LEGION ROAD)

DIVISION 6 CUMBERLAND COUNTY HOPE MILLS

PLAN DATE: SEPTEMBER 2003 REVIEWED BY: *TUJ*

PREPARED BY: WILLIAM HAIRSTON REVIEWED BY:

REVISIONS: _____ INIT. DATE

122 N. McDowell St., Raleigh, NC 27603

SEAL

SIG. INVENTORY NO. 06-0455