NOTES

- I. TO PREVENT "FLASH-CONFLICT" PROBLEMS, WIRE ALL UNUSED PHASES AND OVERLAPS TO FLASH RED. VERIFY THAT SIGNAL HEADS FLASH IN ACCORDANCE WITH THE SIGNAL PLANS.
- 2. TO PREVENT RED FAILURES ON UNUSED MONITOR CHANNELS, TIE UNUSED LOAD SWITCH RED OUTPUTS 4, 6, 7 AND 8 TO LOAD SWITCH AC+ BY INSERTING A JUMPER PLUG IN THE UNUSED LOAD SWITCH SOCKET FROM PIN I(LS AC+) TO PIN 3 (RED OUT). MAKE SURE ALL FLASH TRANSFER RELAYS ARE IN PLACE.
- 3. PROGRAM THE CONTROLLER TO START UP IN PHASE 2 GREEN.
- 4. SET POWER-UP FLASH TIME TO IO SECONDS AND IMPLEMENT ON THE CONFLICT MONITOR. SET CONTROLLER POWER-UP FLASH TIME TO O SECONDS.
- 5. ENABLE SIMULTANEOUS GAP-OUT FEATURE, ON CONTROLLER UNIT, FOR ALL PHASES.
- 6. WIRE DETECTORS IN ACCORDANCE WITH MANUFACTURERS' INSTRUCTIONS TO ACCOMPLISH THE DETECTION SCHEMES SHOWN ON THE SIGNAL DESIGN PLANS.

FIELD CONNECTION HOOK-UP CHART OLA OLB OLC OLD PLD PED PED PED 32 | 21,22 | 31,32 | NU NU NU 23,24 NU NU 23 HEAD NO. 2G | 3G 5Y **2Y** YELLOW 2R 5R 3R ARROW YELLOW ARROW GREEN

PROJECT REFERENCE NO.

R-2417BB

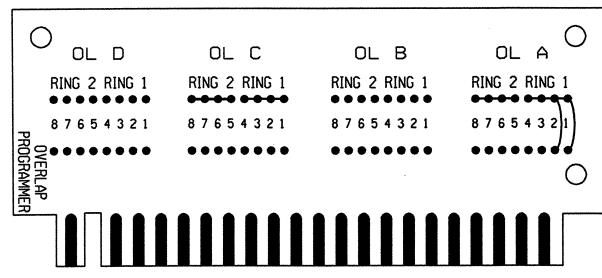
Sig.

NU = NOT USED

ARROW

* DENOTES INSTALL LOAD RESISTOR. SEE LOAD RESISTOR INSTALLATION DETAIL THIS PAGE.

NEMA OVERLAP CARD

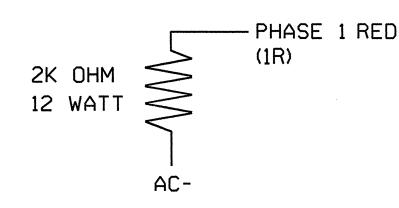


INSTALL JUMPERS AS SHOWN

EQUIPMENT INFORMATION

CONTROLLER	
CABINET	
LOADBAY POSITIONS	
LOAD SWITCHES USED	
PHASES USED	
OL/A	
OL/C	
OL/D	NOT USED

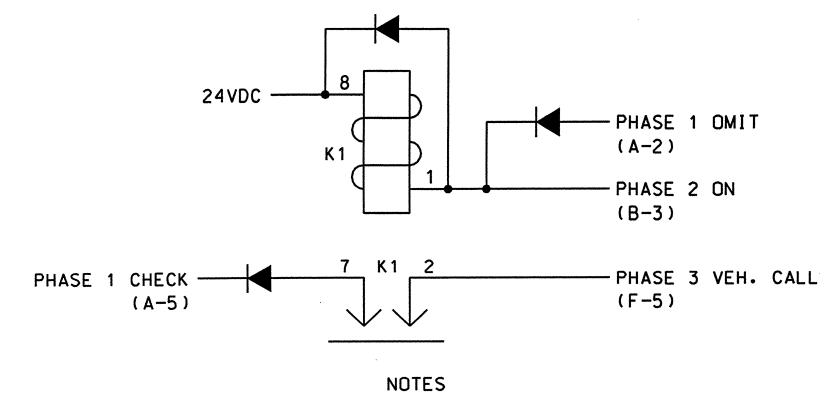
LOAD RESISTOR INSTALLATION DETAIL



NOTE: THE PURPOSE OF THIS RESISTOR IS TO LOAD THE CHANNEL RED MONITOR INPUT IN ORDER FOR THE SIGNAL SEQUENCE MONITOR TO USE THE FULL SIGNAL SEQUENCE MONITORING CAPABILITY ON PHASES THAT DO NOT USE THE RED DISPLAY IN THE FIELD.

BACK-UP PROTECTION WIRING DETAIL

(WIRE AS SHOWN)



- 1. RELAY 'K1' IS A SPST WITH A 24VDC COIL. (P&B# KRP3DH)
- 2. ALL DIODES ARE VALUED AT 600V PIV. 1 AMP MINIMUM. (RECOMMENDED PART NO. 1N4005)
- 3. WHEN TRAFFIC CONDITIONS REQUIRE THE CONTROLLER TO BACK-UP FROM PHASE 2 TO PHASE 1. THIS RELAY LOGIC CIRCUIT WILL FORCE THE CONTROLLER TO CYCLE THROUGH PHASE 3. THE CONTROLLER IS NOT ALLOWED TO BACK-UP DIRECTLY TO PHASE 1 FROM PHASE 2.

TYPICAL CONNECTION CHART FOR DETECTORS

PIN FUNCTION TERMINATION

I III I DINCI I DIN	I ETTIMIZITATI I OTT
AC+	AC+
AC-	AC-
CHASSIS GROUND	CHASSIS GROUND
LOOP INPUT	LOOP
LOOP INPUT	LOOP
RELAY NORMALLY OPEN	VEHICLE CALL INPUT
RELAY COMMON	LOGIC GROUND
TIMER INHIBIT	ASSOCIATED PHASE GREEN

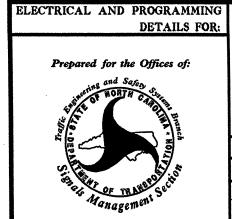
NOTES:

- I. THE TIMER INHIBIT WIRE SHALL BE CONNECTED TO THE ASSOCIATED PHASE GREEN LOAD SWITCH OUTPUT WHEN ONLY DELAY OPERATION IS REQUIRED, UNLESS OTHERWISE SPECIFIED.
- 2. IF EXTEND OPERATION IS REQUIRED, THE TIMER INHIBIT WIRE SHALL NOT BE CONNECTED.

HNTB

HNTB NORTH CAROLINA, P.C. 343 E. Six Forks Road, Suite 200 Raleigh, North Carolina 27609 THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 08-0649
DESIGNED: 06-2004
SEALED: 06-14-04
REVISED: N/A

Signal Upgrade - Temporary Design 1



NC 42/SR 1579 (Broadway Rd.)
At
SR 1529 (Cox Mill Road)

Division 8 Lee County Sanford
PLAN DATE: JUNE 2004 REVIEWED BY: H.L. WINSTEAD
PREPARED BY: K.H. IDE REVIEWED BY:

DATE: JUNE 2004 REVIEWED BY: H.L. WINSTEAD
RED BY: K.H. IDE REVIEWED BY:
REVISIONS INIT. DATE

